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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Lothar Bauch et al.
Appl. No. : 10/082,554
Filed : February 26, 2002
Title : Stacked VIA with Specially Designed Landing
Pad for Integrated Semiconductor Structures
Examiner : Pershelle L. Greene

*\$9/Lothar Bauch
Amend
3/5/03*

REQUEST TO APPROVE DRAWING CHANGES

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Please approve the drawing changes - adding a first layer, a second layer, and an intermediate layer to Fig. 1, and two adjacent points of a periodic grid to Fig. 3 - which are marked in red on the accompanying mark-up drawings. Also enclosed are "final" drawings.

Respectfully submitted,

Markus Nollf

For Applicants

MARKUS NOLFF
REG. NO. 37,006

MN:cgm

Date: January 6, 2003

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